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	Subclass
Class	ISSUE CLASSIFICATION

PATENT NUMBER	
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U.S. UTILITY Patent Application

O.I.P.E. M H T K I SCANNED	PATENT DATE Q.A. 07/26/01
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/888607	F	702 711	42	2857 2114	BEAUSUE 2114
APPLICANTS	Robert Abbott				
TITLE	Method of testing embedded memory array and embedded memory controller for use therewith				
PTO-2040 12/99					

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	5 (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
				ISSUE BATCH NUMBER	
(Legal Instruments Examiner) _____ (Date)					

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